



2827

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cyril Cabral Jr.

Group Art Unit: 2827

Serial No.: 09/686,742

Examiner: Lourdes Cruz

Filed: Oct. 11, 2000

In Response to Office Action
Dated: April 7, 2003

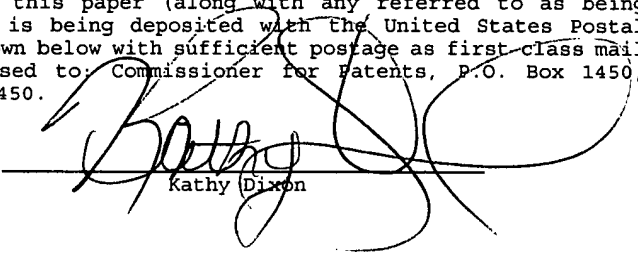
For: Semiconductor Structure Having In-Situ Formed
Unit Resistors and Method for Fabrication

Attorney Docket No.: 64,610-057 (YOR9-2000-0361US1)

Certificate of Mailing

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: May 7, 2003


Kathy Dixon

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to an Office Action mailed April 7, 2003,
please enter the following amendments and consider the following
remarks.

U.S.S.N. 09/686,742

Figure 3A has been amended to show the third insulating material layer recited in claim 15. A redline copy of Figure 3A is attached hereto for the Examiner's approval.

In the event that the present invention is not in a condition for allowance for any other reasons, the Examiner is respectfully invited to call the Applicants' representative at his Bloomfield Hills, Michigan office at (248) 540-4040 such that necessary action may be taken to place the application in a condition for allowance.

Respectfully submitted,

Tung & Associates

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RWT\kd